

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Docket No.

Applicant(s): John M. Cohn, et al.

BUR920040001US1 (17382)

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/709,754	May 26, 2004	John P. Trimmings	23389	2117	3753

Invention: A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	26 -	30 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	4 -	4 =	0	x \$210.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- No additional fee is required for amendment.
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Signature

Dated: November 16, 2007

Steven Fischman

Registration No. 34,594

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